

REMARKS

Claims 1-8, 13, 14 and 20-34 are pending in this application. By this Amendment, claims 1-4, 6, 23, 24 and 28 are amended. No new matter is added. Applicant respectfully requests reconsideration and prompt allowance of the pending claims at least in light of the following remarks.

Claims 1, 6, 23 and 24 are amended by incorporating the previously canceled feature of claim 5.

Entry of the amendments is proper under 37 CFR §1.116 since the amendments: (a) place the application in condition for allowance (for the reasons discussed herein); (b) do not raise any new issue requiring further search and/or consideration (since the amendments amplify issues previously discussed throughout prosecution); (c) satisfy a requirement of form asserted in the previous Office Action; (d) do not present any additional claims without canceling a corresponding number of finally rejected claims; and (e) place the application in better form for appeal, should an appeal be necessary. Entry of the amendments is thus respectfully requested.

Claim 1 is objected to as including informalities. The objection is obviated by the above amendment.

Claim 2 is rejected under 35 U.S.C. §112, second paragraph, as being indefinite. The rejection is obviated by the above amendment.

Claims 1-8, 13, 14, 20-26 and 28-34 are rejected under 35 U.S.C. §103(a) over U.S. Patent No. 6,956,547 (Bae) in view of U.S. Patent Application Publication No. 2002/0033783 (Koyama); and claim 27 is rejected under 35 U.S.C. §103(a) over Bae in view of Koyama and further in view of U.S. Patent Application Publication No. 2002/0171607 (Senda). The rejections are respectfully traversed.

Regarding the rejections of claim 1, Bae and Koyama, alone or in combination, do not disclose or suggest an electronic circuit comprising "an amount of charge held in the capacitor being reset to a predetermined state under when a second transistor is turned on," as recited in amended claim 1.

As can be seen in Figs. 2, 4, 5, 7, 9, 10 and 12, Bae does not disclose any transistor that resets the capacitor C1. In Bae's voltage mode, the resetting of capacitor is not required because the driving transistor PM 11 ON/OFF is controlled by PM 13 transistor of which gate is controlled by Gate Driver Unit 200 as shown in Fig. 7.

As shown in Fig. 15B, Koyama also does not disclose or suggest any transistor that resets the capacitor. In Koyama's time-gradation mode, the gate of the driving transistor is controlled by source signal line through TFT 1514, forcing charging and discharging of the capacitor 1516, thus obviating any need for resetting the capacitor charge.

As discussed above, the foregoing feature was included in original claim 5. In the June 28, 2007 Office Action, claim 5 was rejected under 35 U.S.C. §103(a) over U.S. Patent Application Publication No. 2003/0052614 (Howard) in view of U.S. Patent No. 6,299,506 (Dawson).

The Office Action relies on Dawson for the feature added to claim 1 by the above amendments. However, when the transistor 270 of Dawson (alleged second transistor) is turned on, it connects the supply voltage +VDD to the source of the pixel driving transistor 260, and does not reset the capacitor 280. Accordingly, claim 1 is patentable over the references of record.

Claims 6, 23 and 24 recite similar features to those referred to in connection with claim 1. Thus, claims 1, 6, 23 and 24 are patentable over the applied references.

Claims 2-5, 7, 8, 13, 14, 20-22 and 25-34 directly or indirectly depend from claims 1, 6, 23 or 24. Thus, claims 2-5, 7, 8, 13, 14, 20-22 and 25-34 are also patentable over the

applied references for at least the same reason discussed above, as well as for the additional features they recite.

For the foregoing reasons, withdrawal of the rejections is respectfully requested.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of the pending claims are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,


James A. Oliff
Registration No. 27,075

Aaron L. Webb
Registration No. 56,930

JAO:HXT/amw

Date: May 14, 2008

OLIFF & BERRIDGE, PLC
P.O. Box 320850
Alexandria, Virginia 22320-4850
Telephone: (703) 836-6400

<p>DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461</p>
--